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Fig. 1

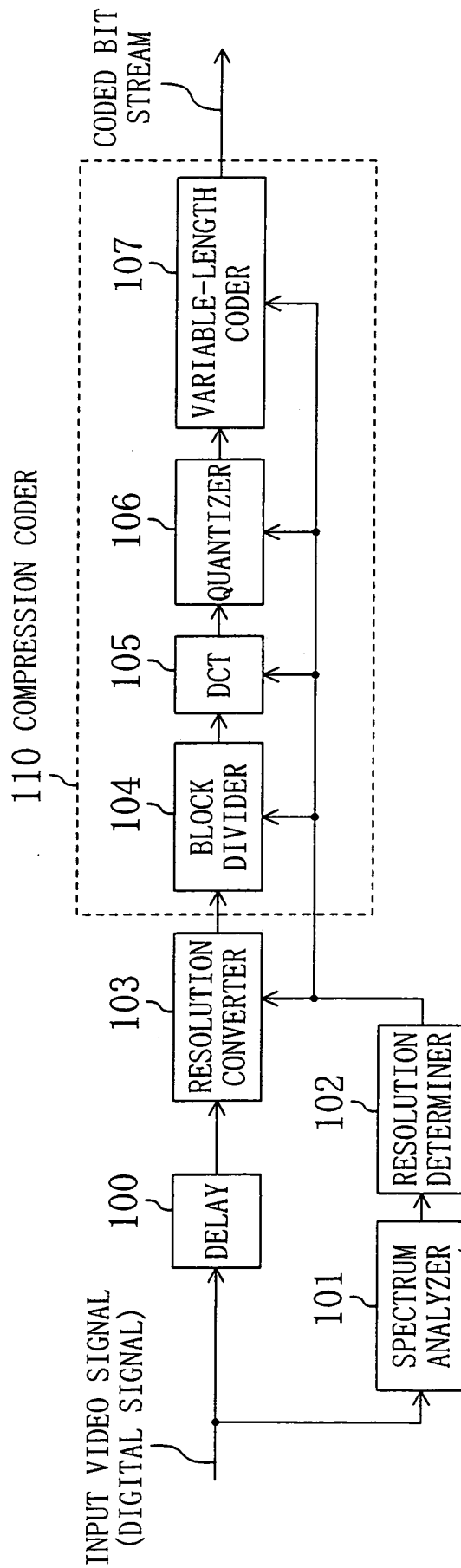


Fig. 2A

resolution of
input video
signal

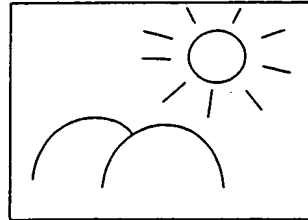


Fig. 2B

resolution A

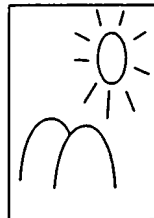


Fig. 2C

resolution B

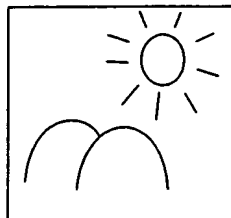


Fig. 2D

resolution C

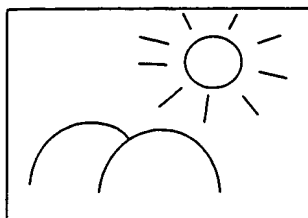


Fig. 3

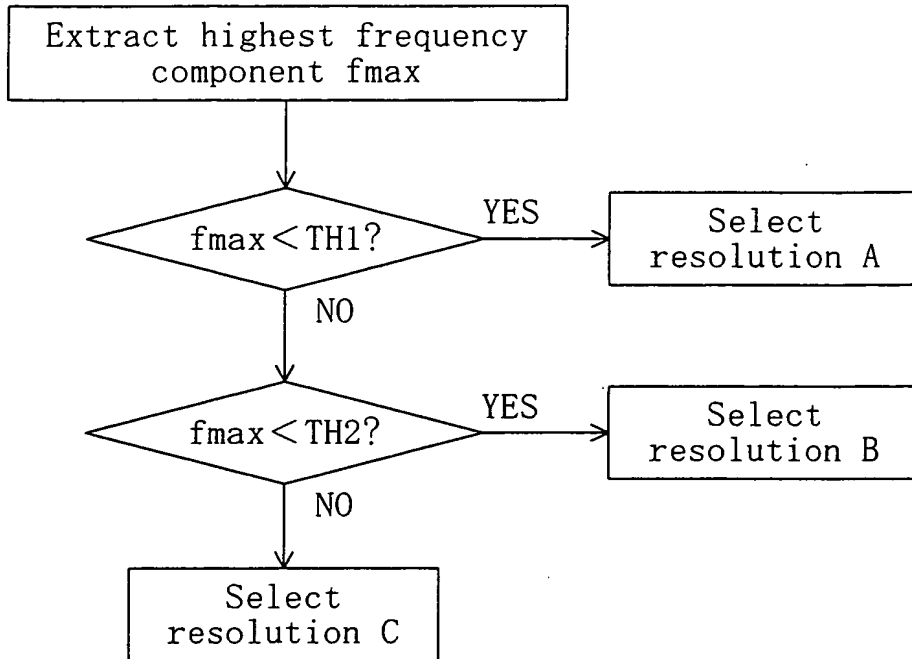


Fig. 4A

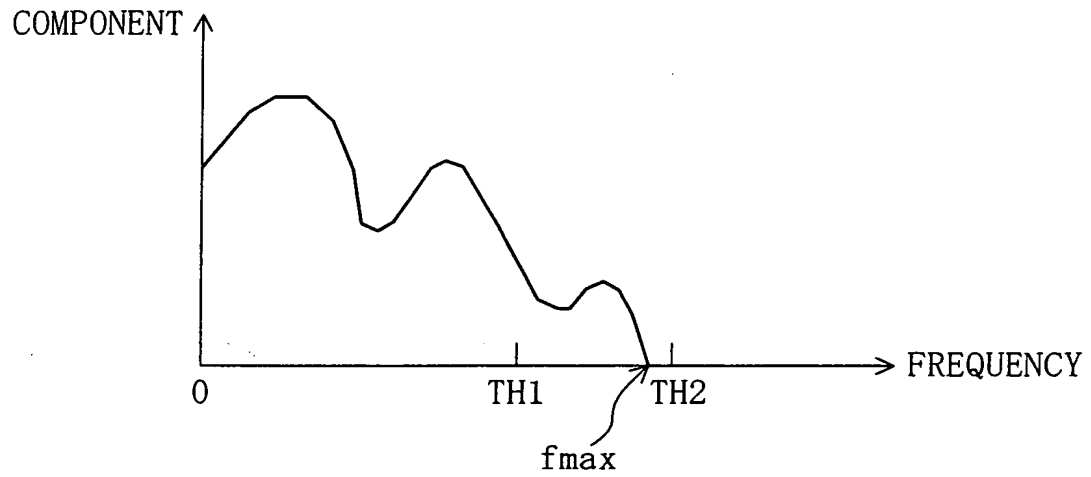


Fig. 4B

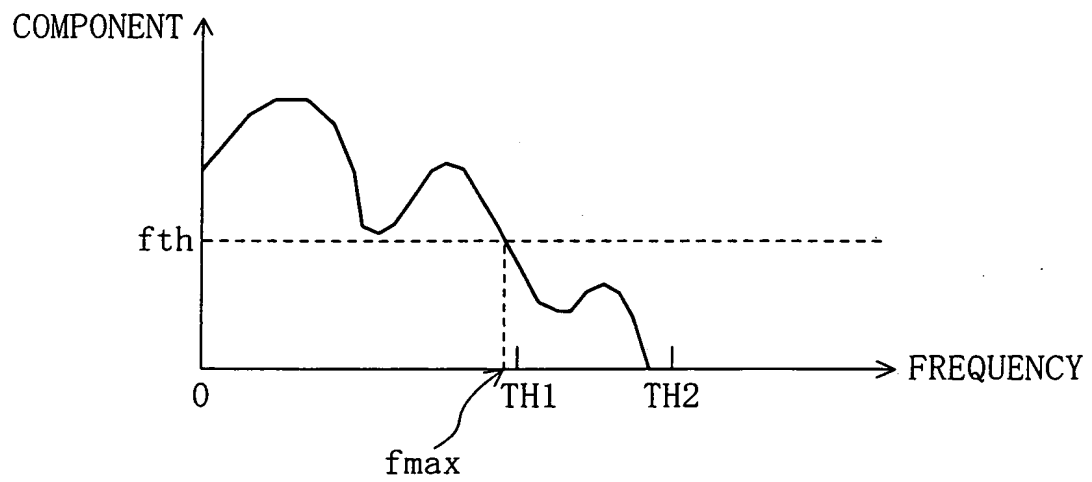


Fig. 5

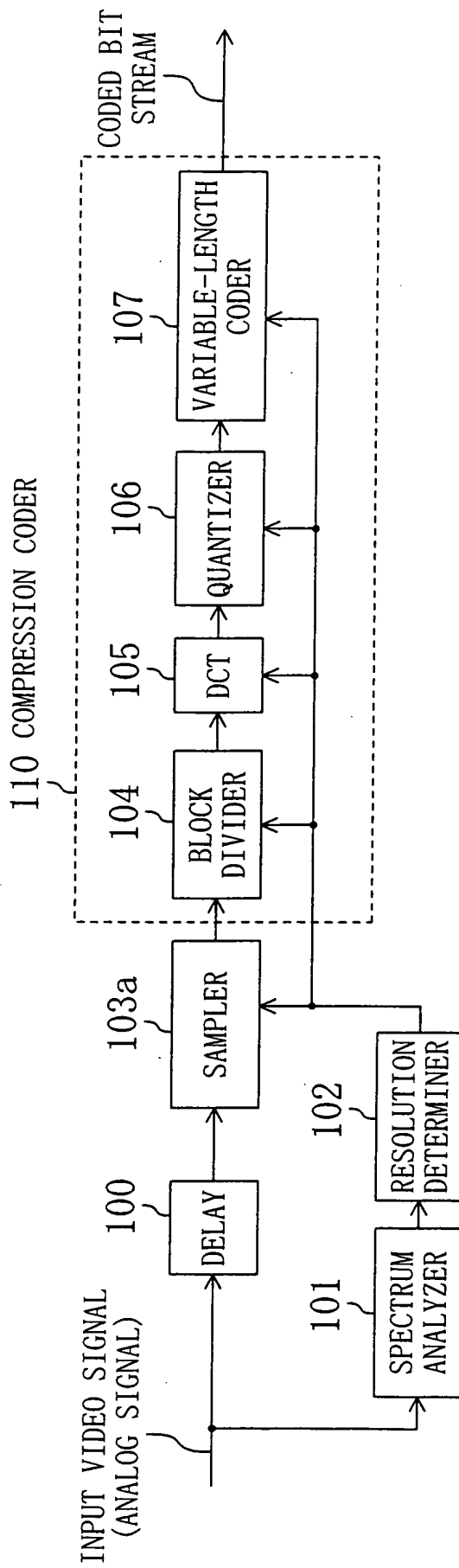


Fig. 6

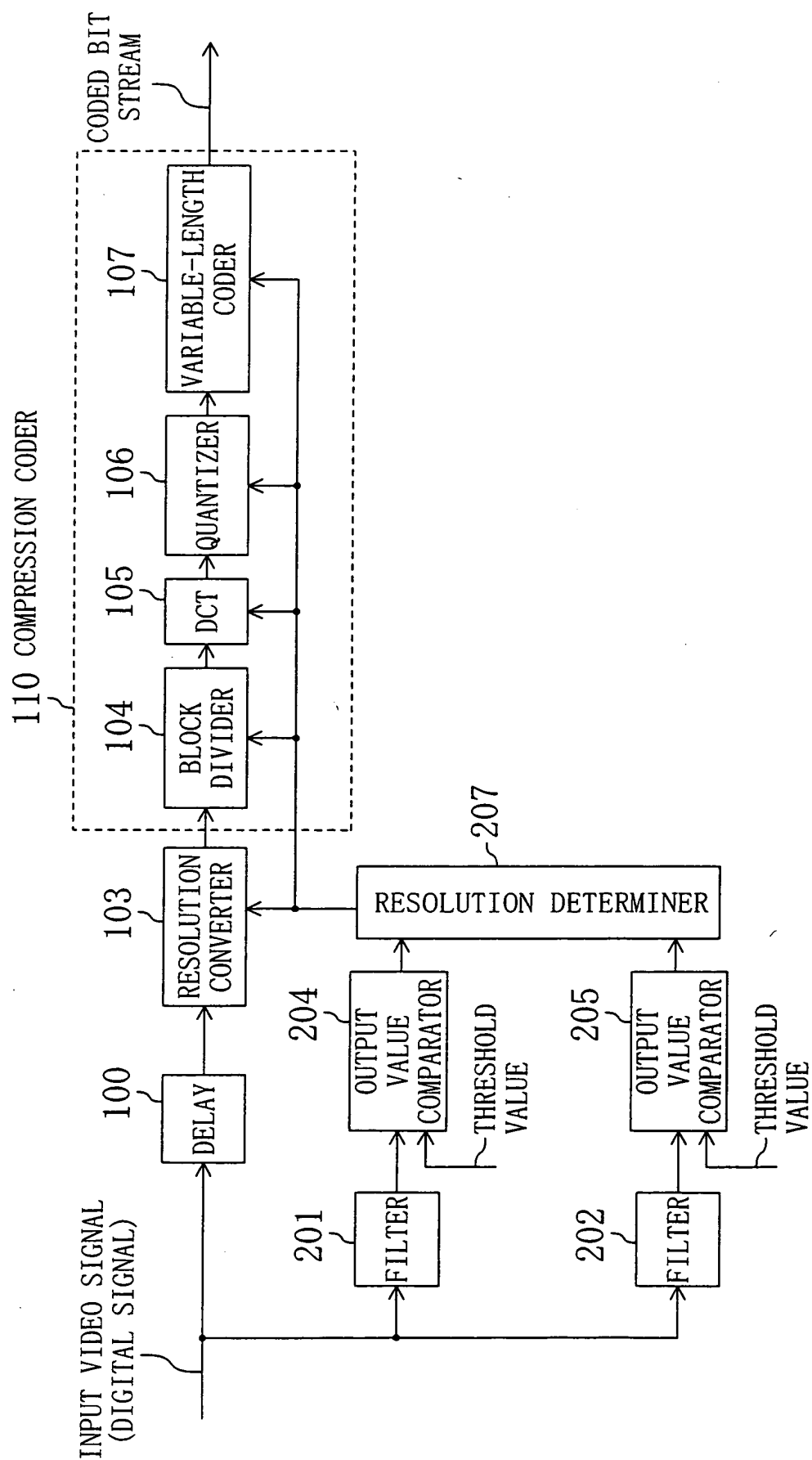


Fig. 7

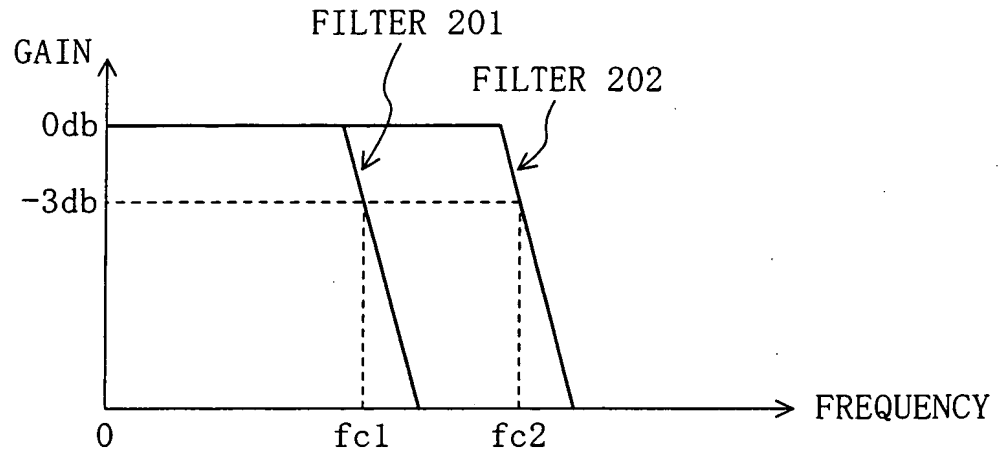


Fig. 8

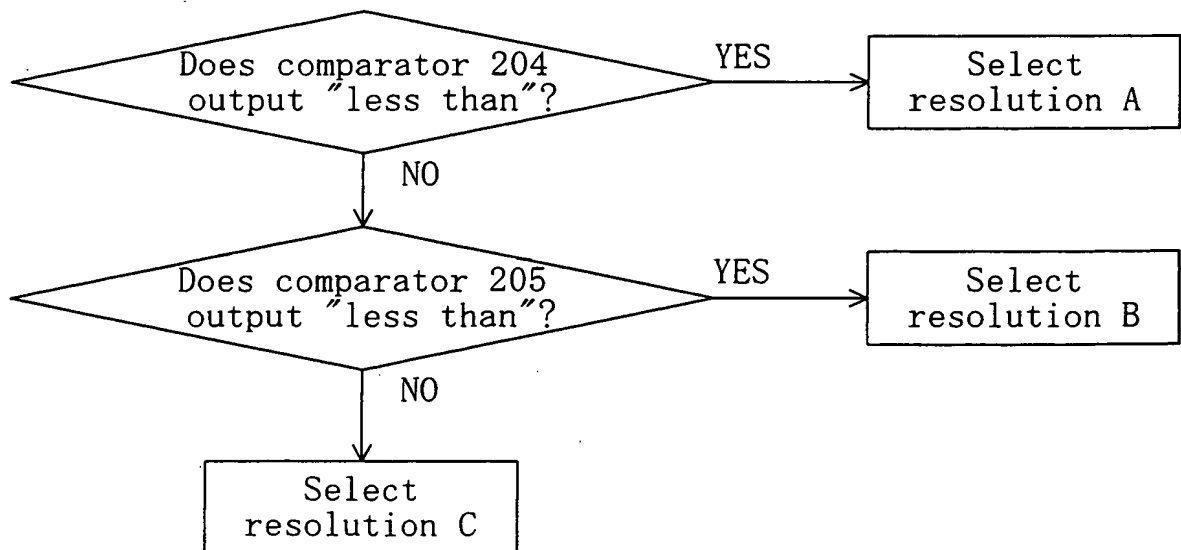


Fig. 9

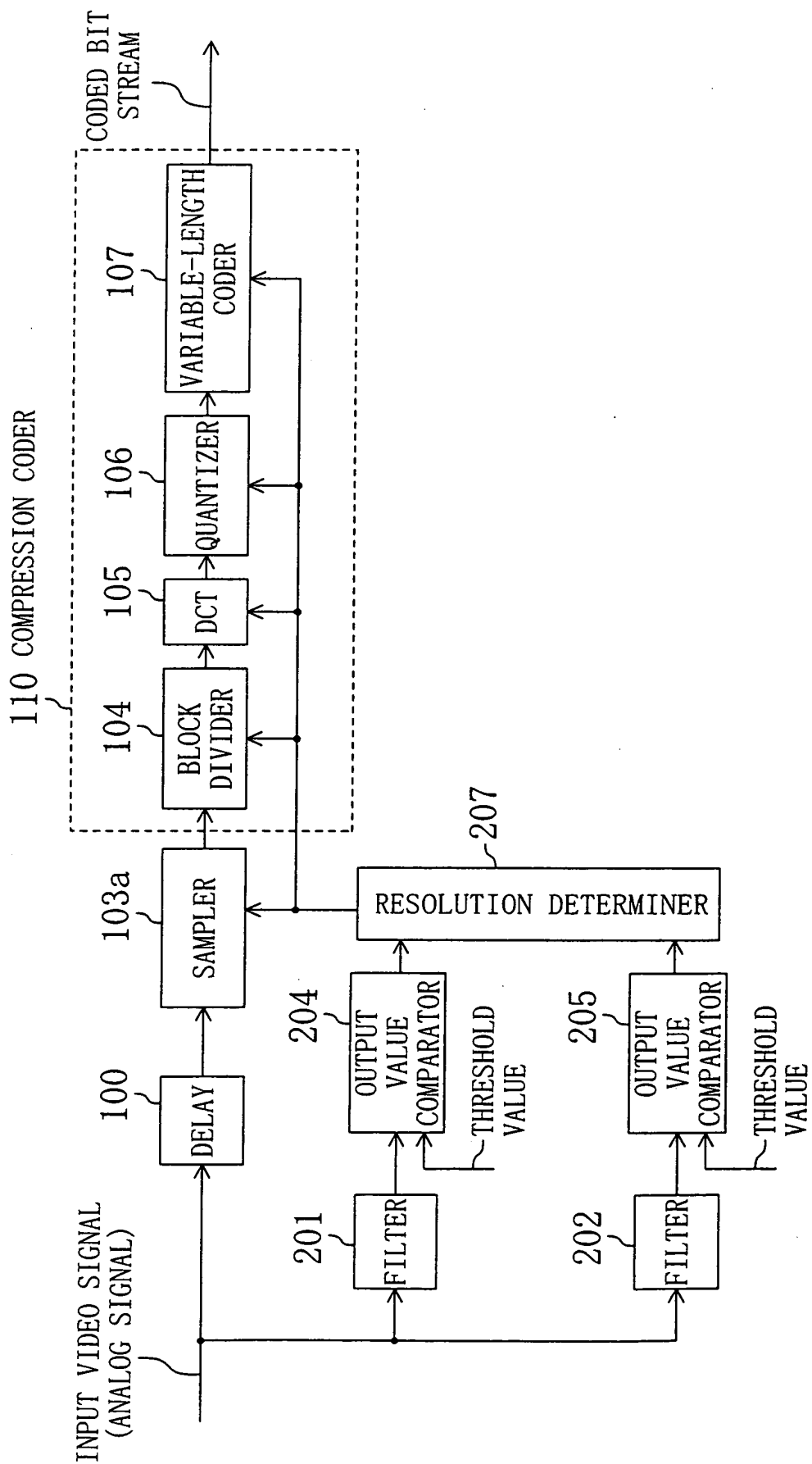


Fig. 10

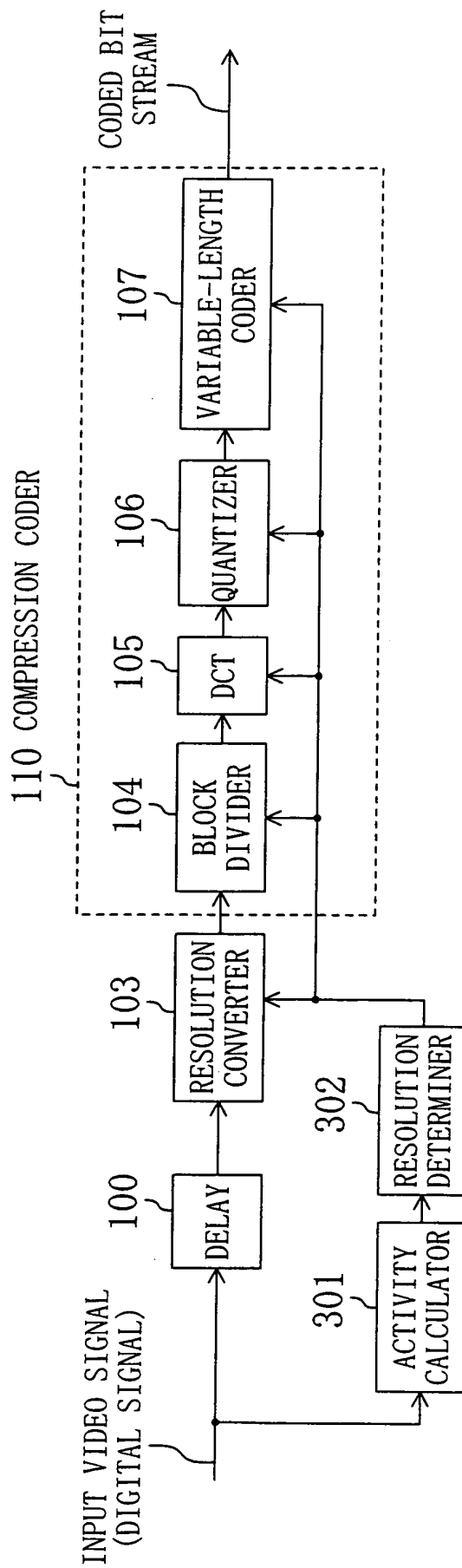


Fig. 11

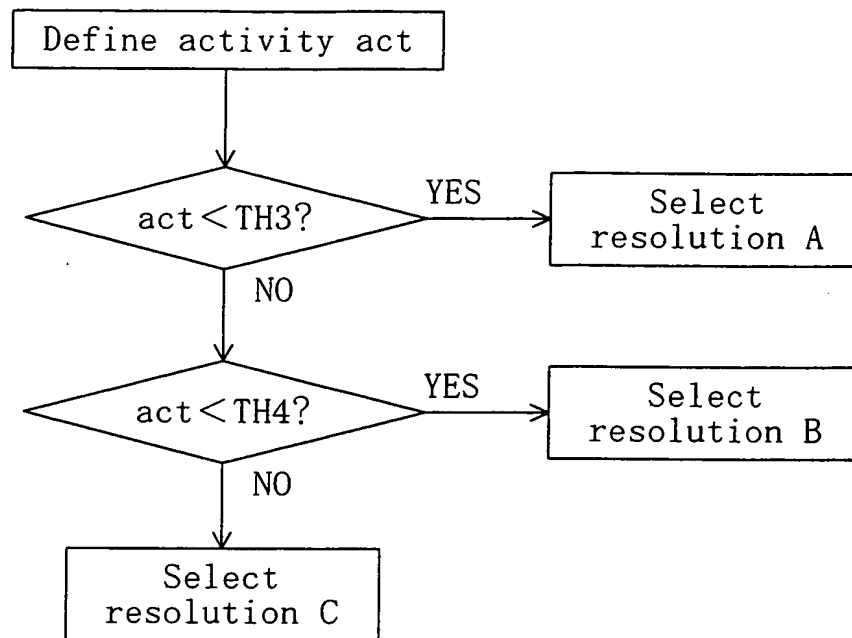


Fig. 12

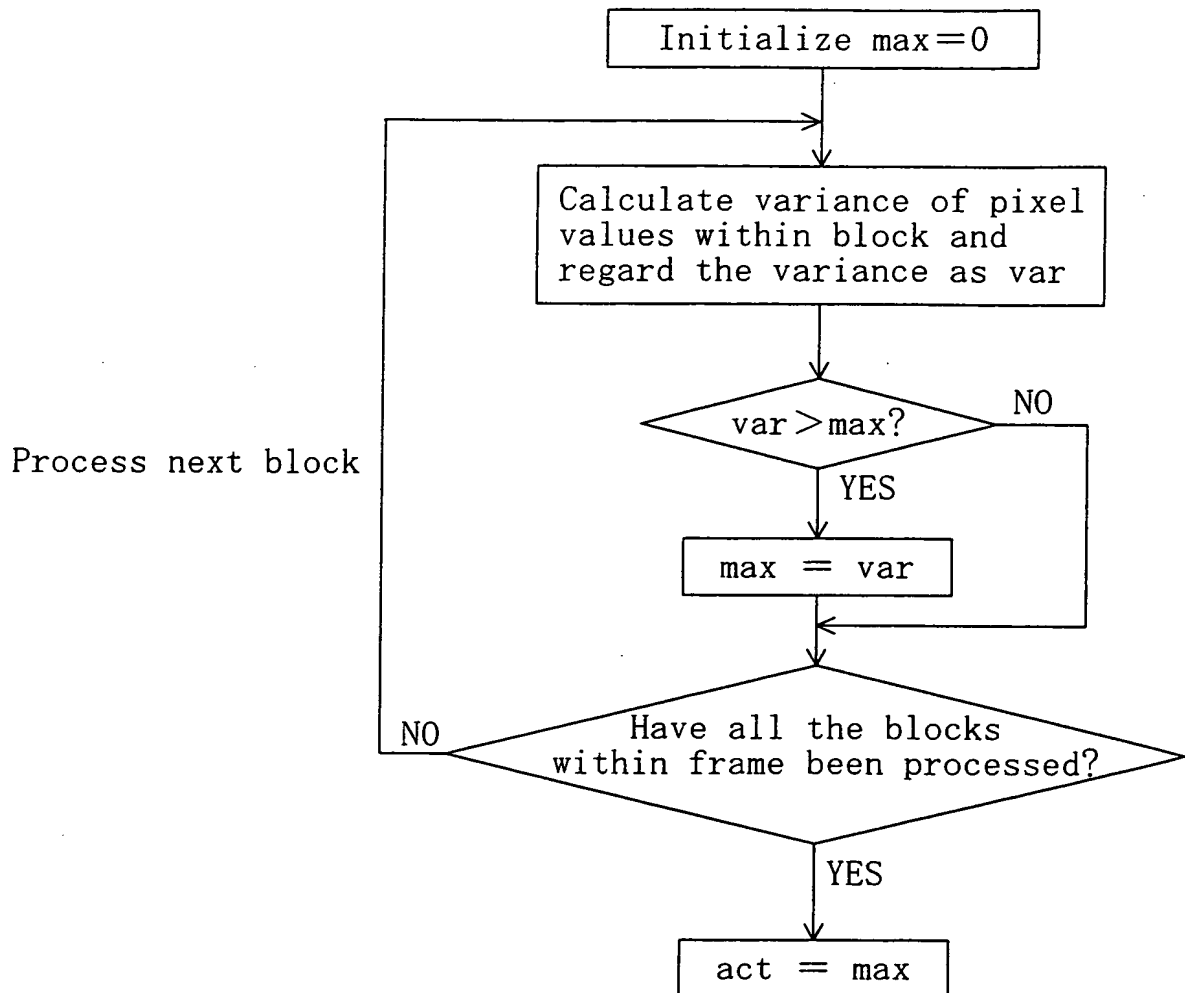


Fig. 13

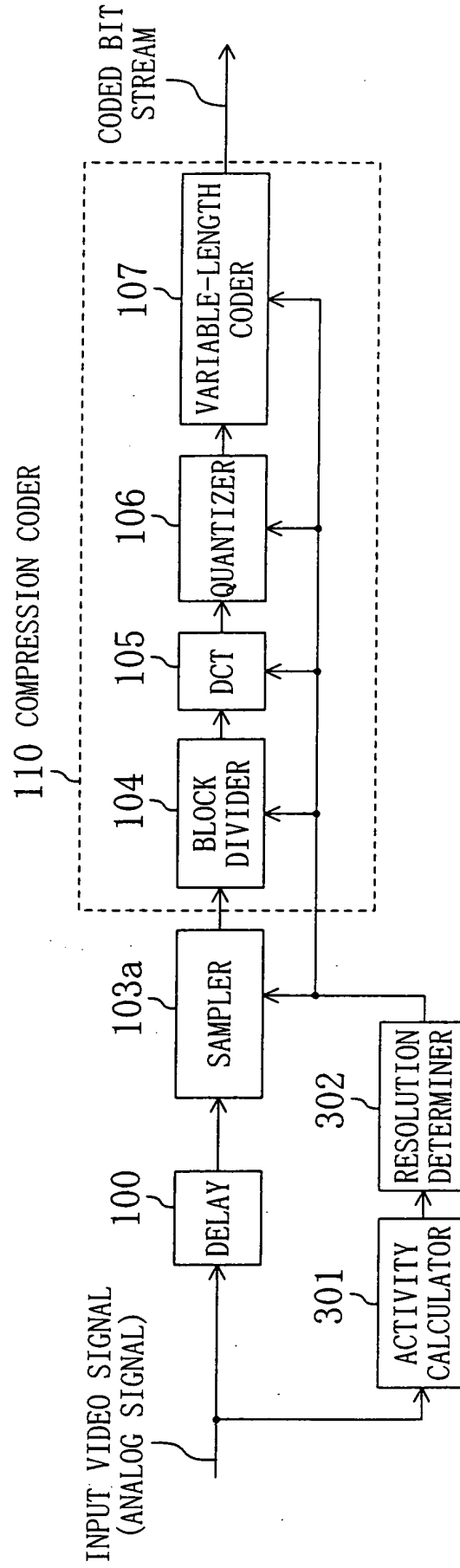


Fig. 14

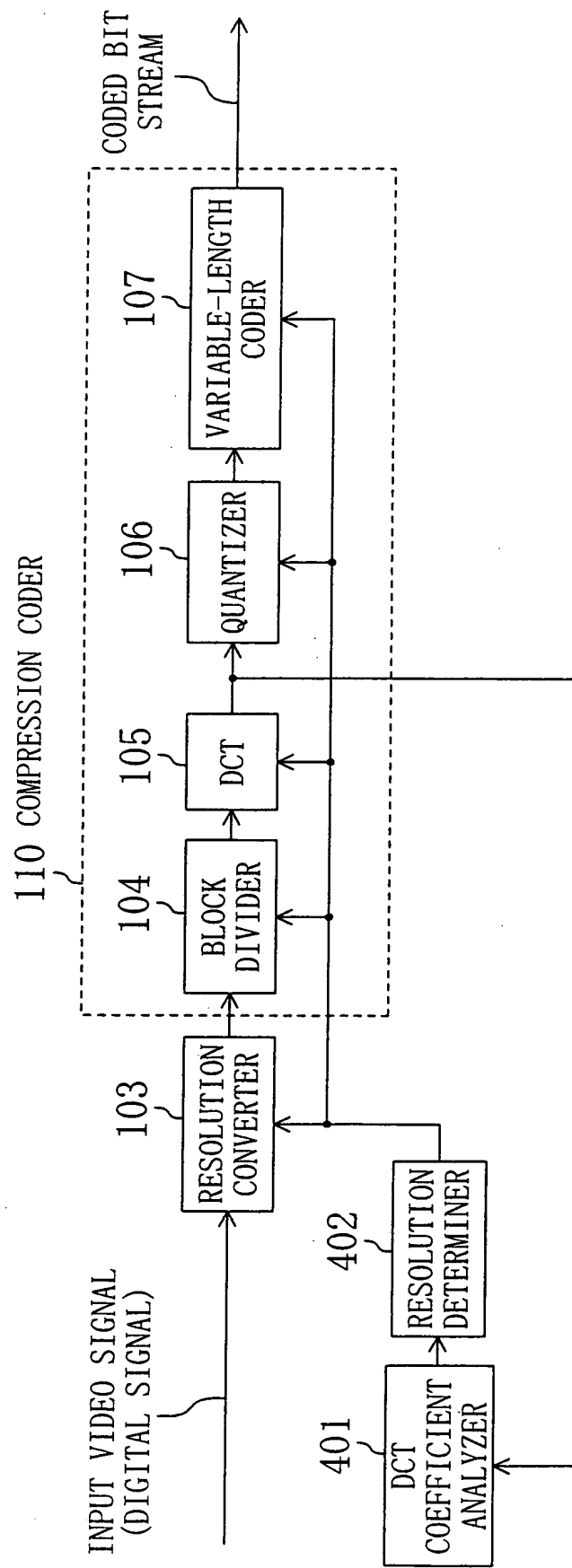


Fig. 15

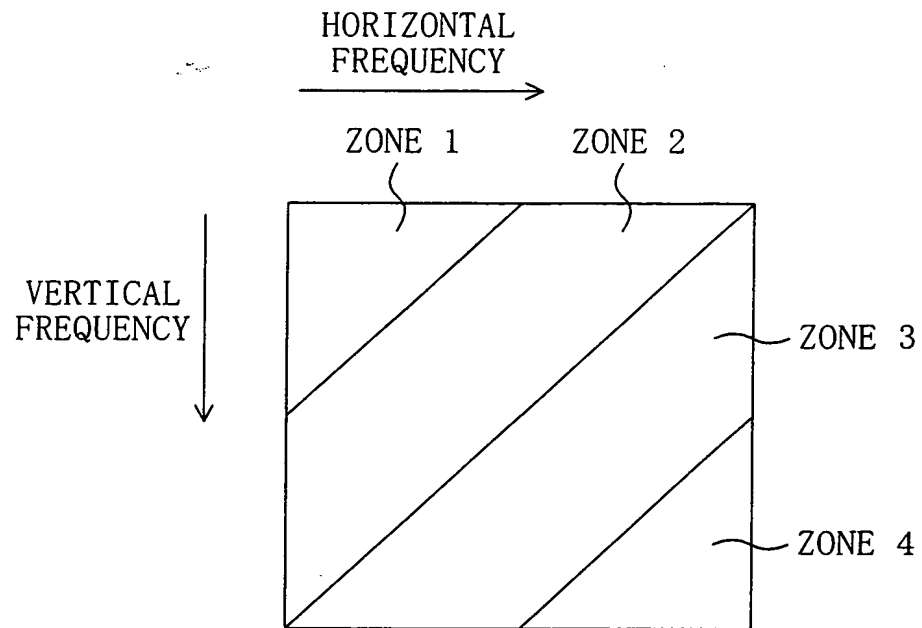


Fig. 16

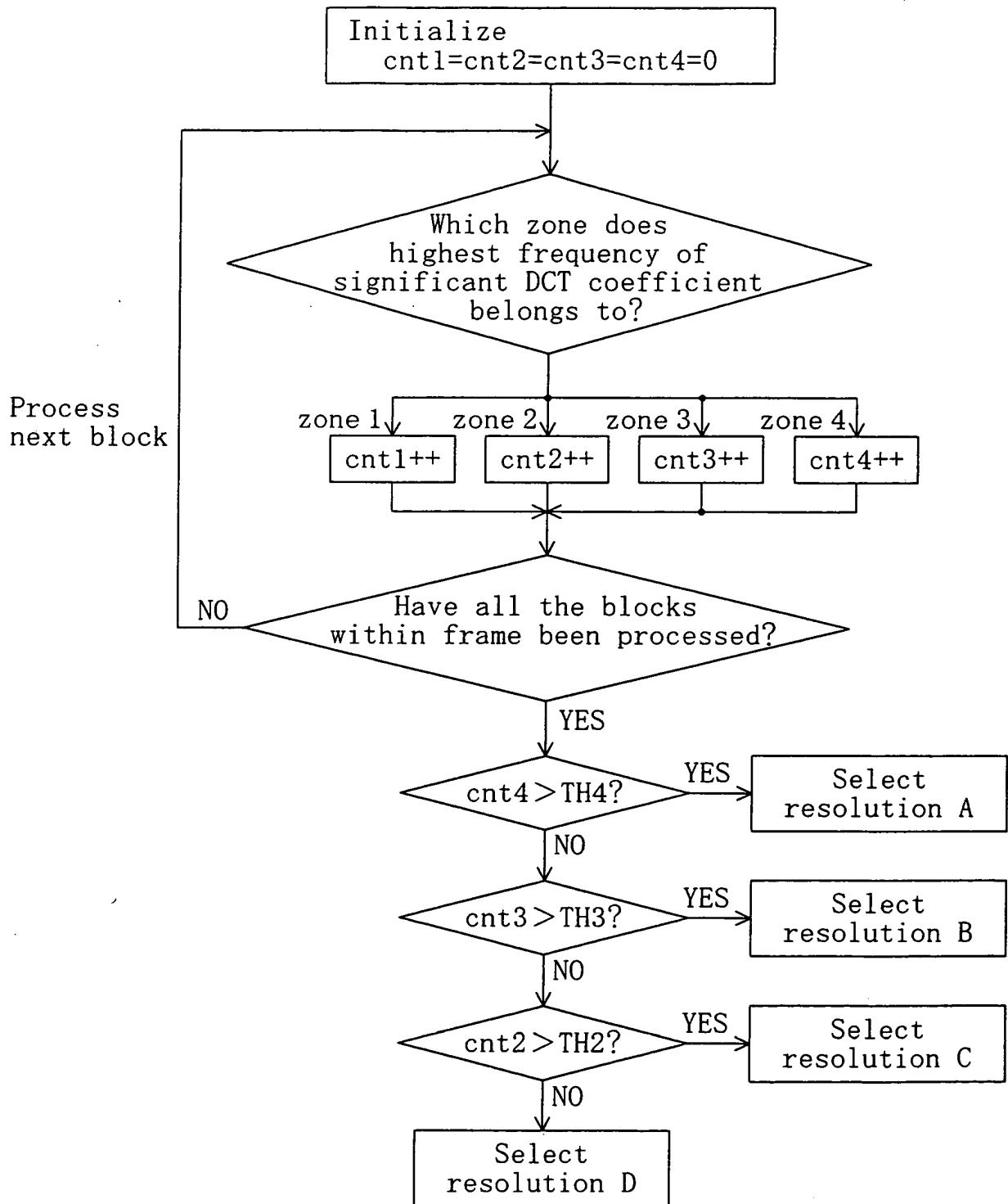


Fig. 17

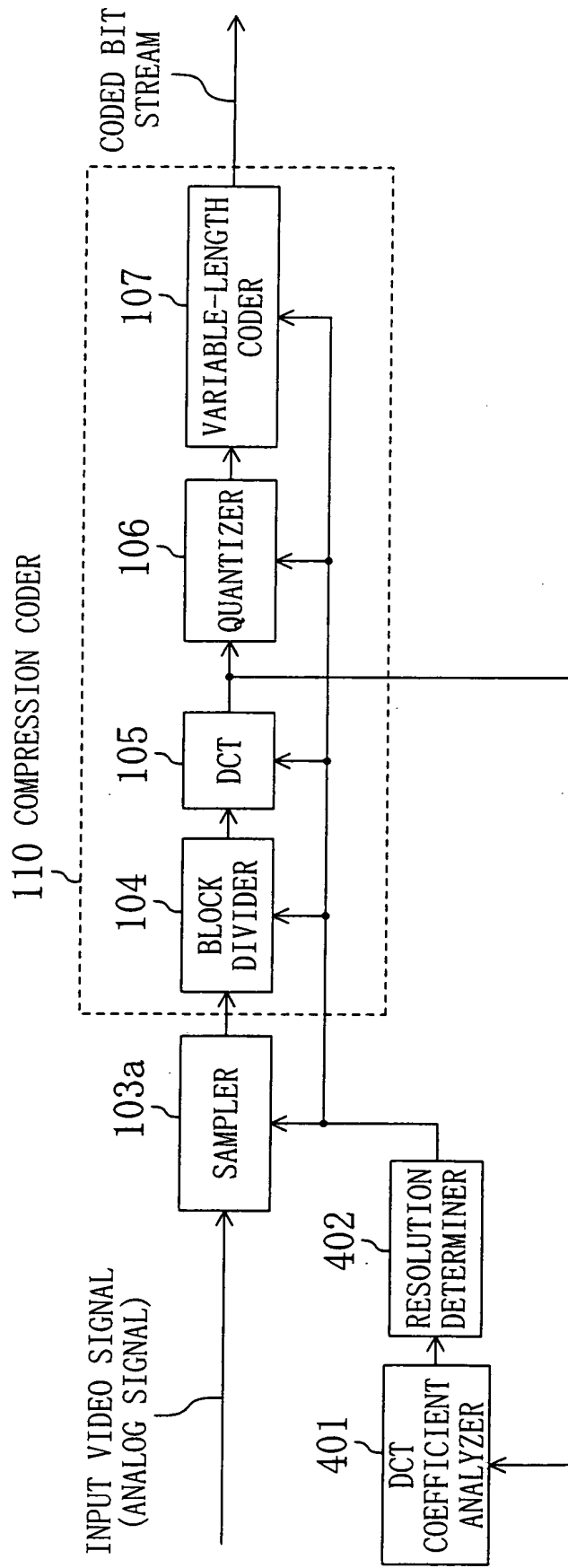


Fig. 18

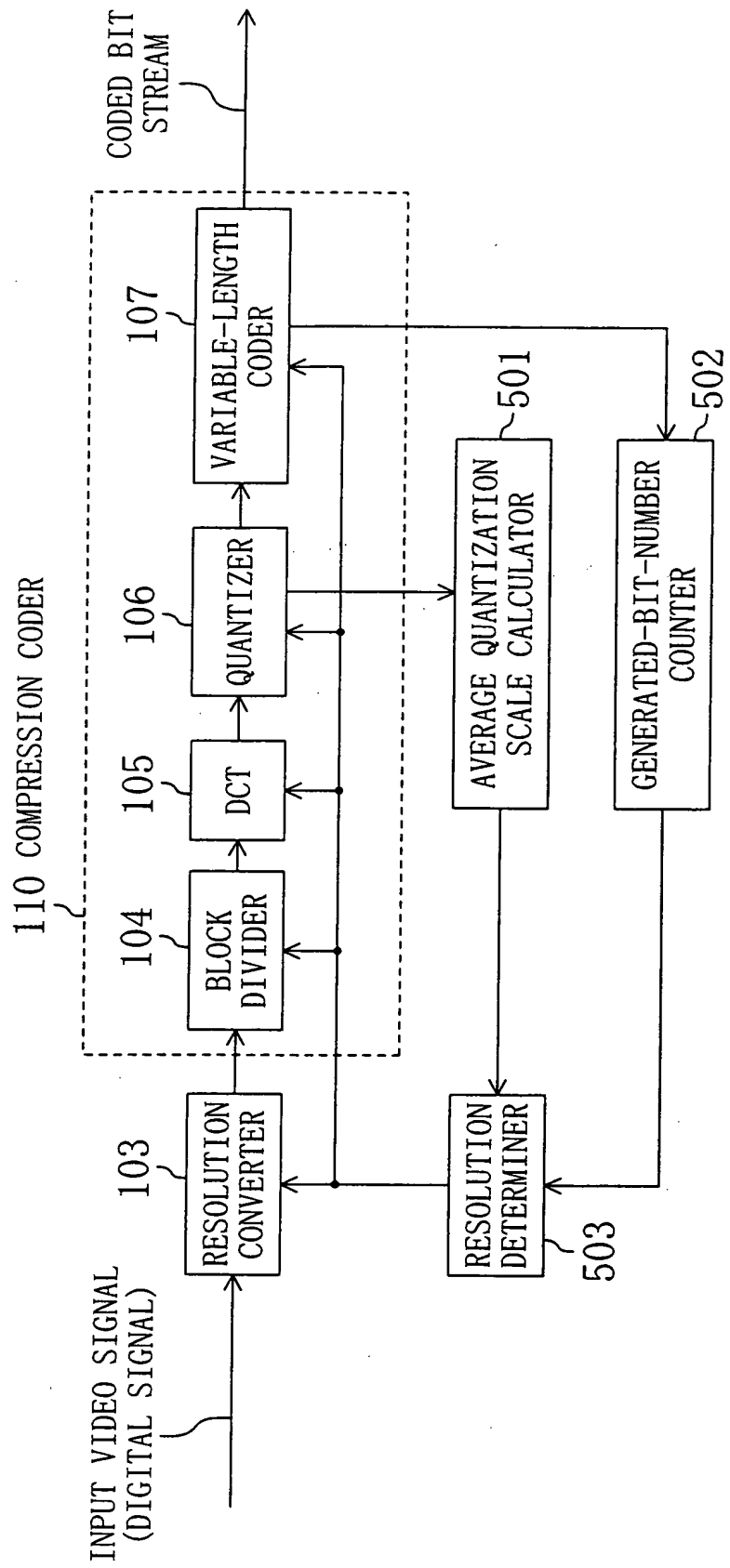


Fig. 19

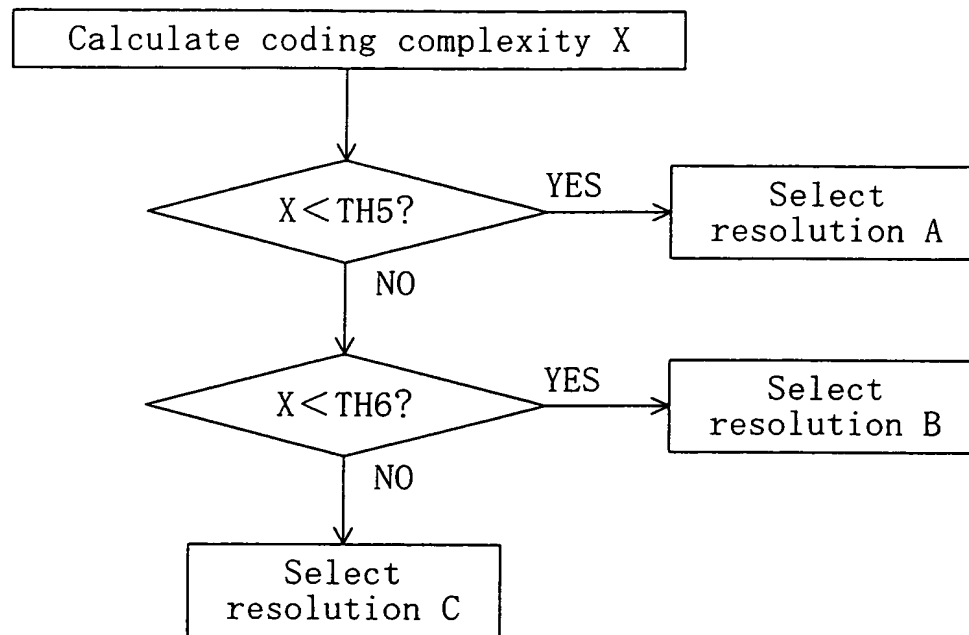


Fig. 20

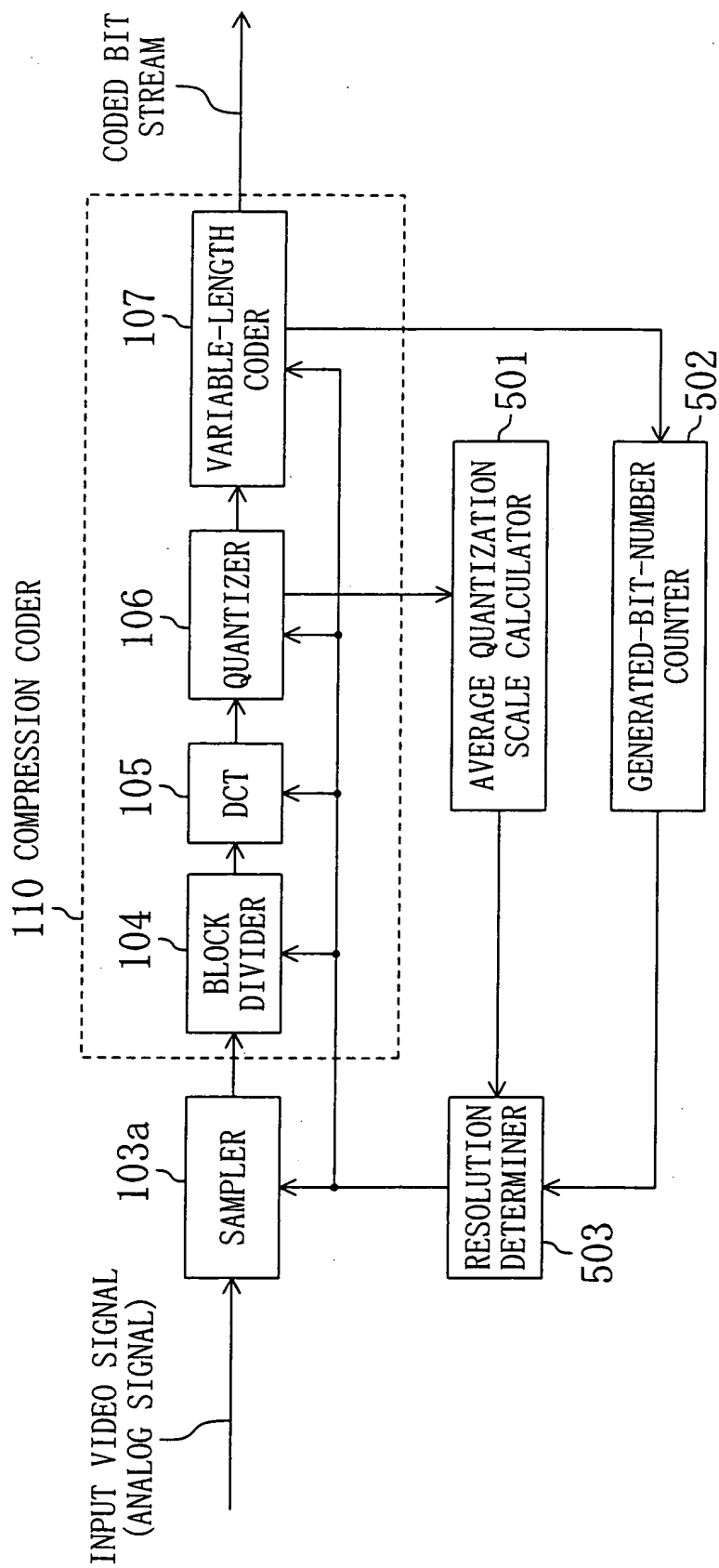


Fig. 21

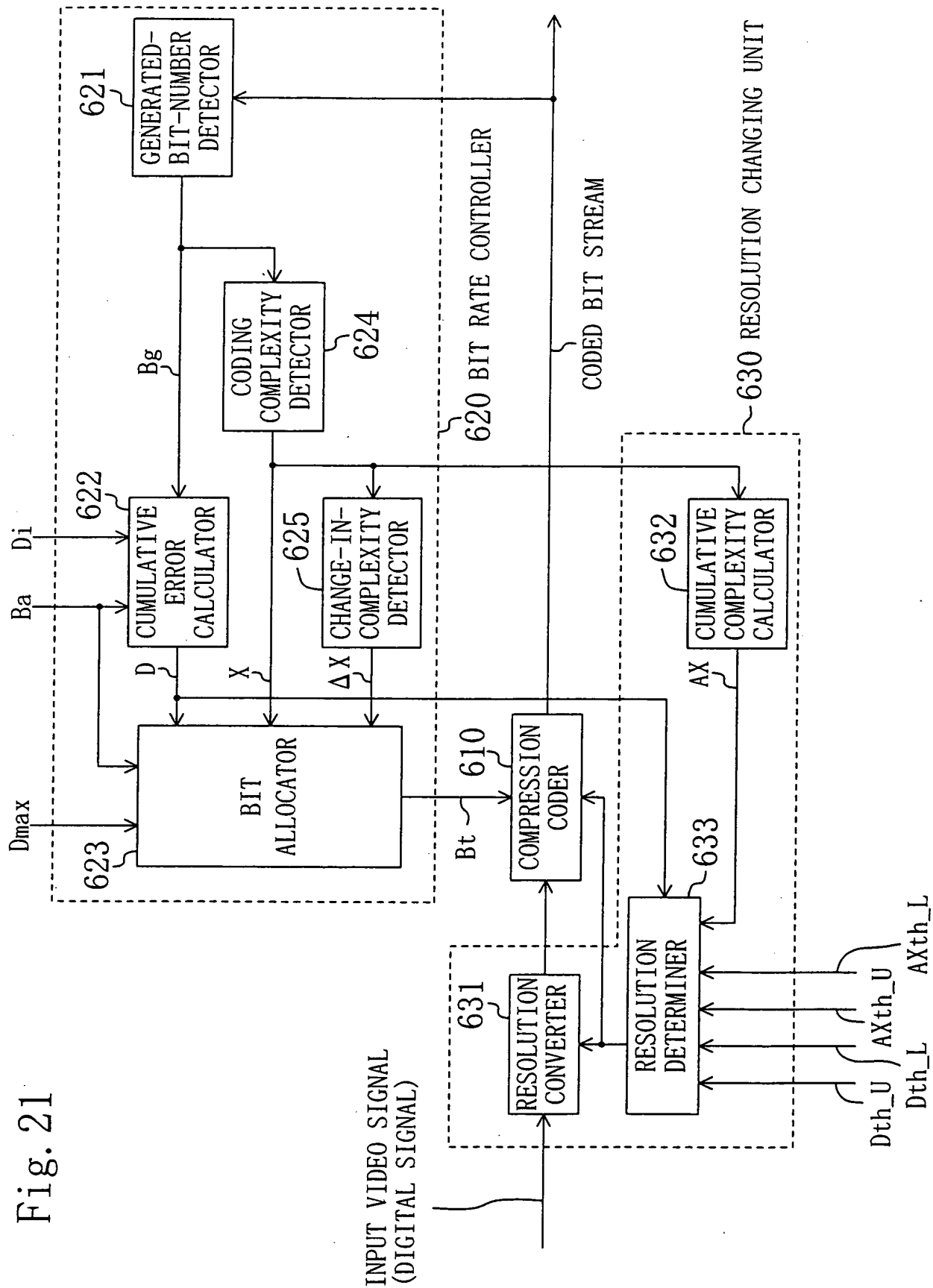


Fig. 22A

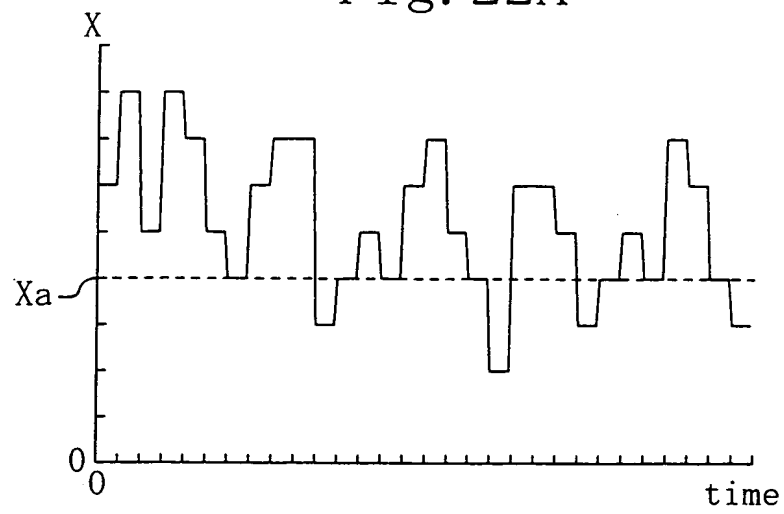


Fig. 22B

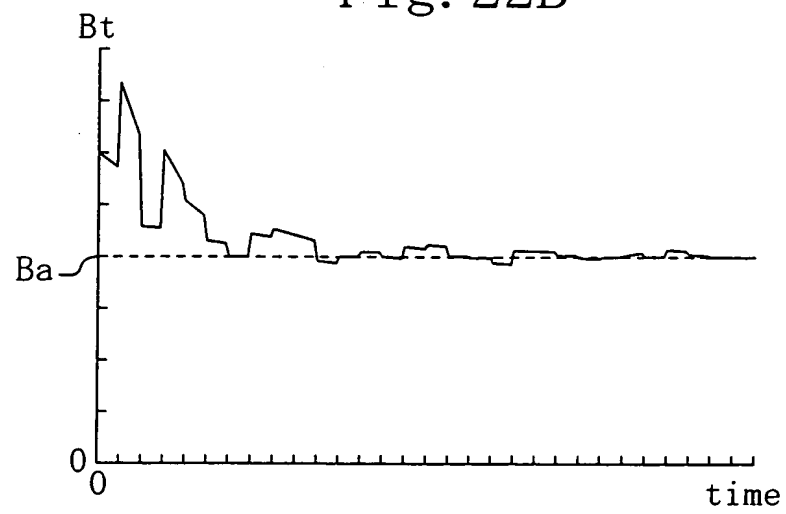


Fig. 22C

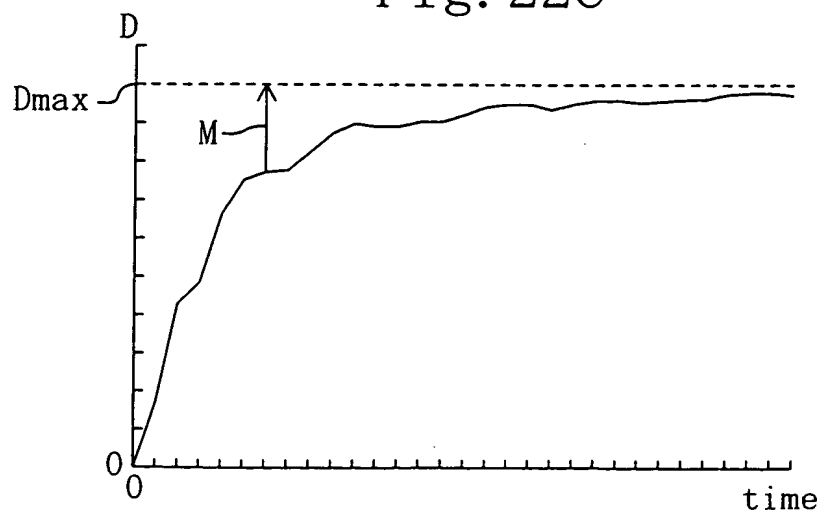


Fig. 23A

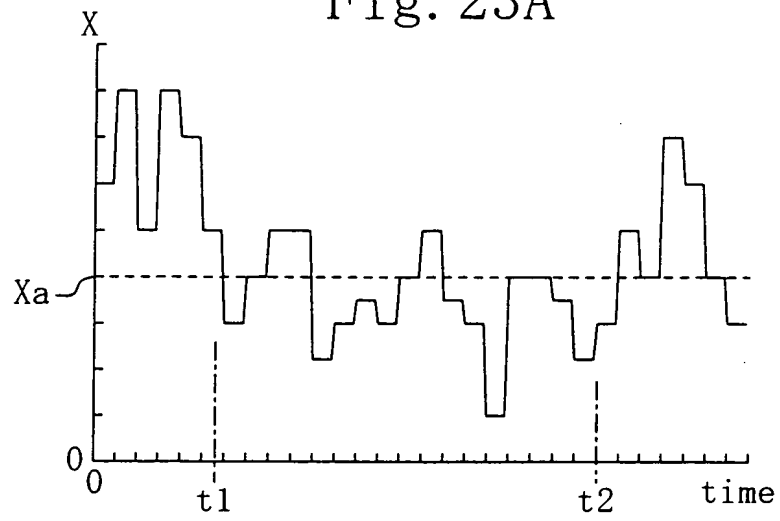


Fig. 23B

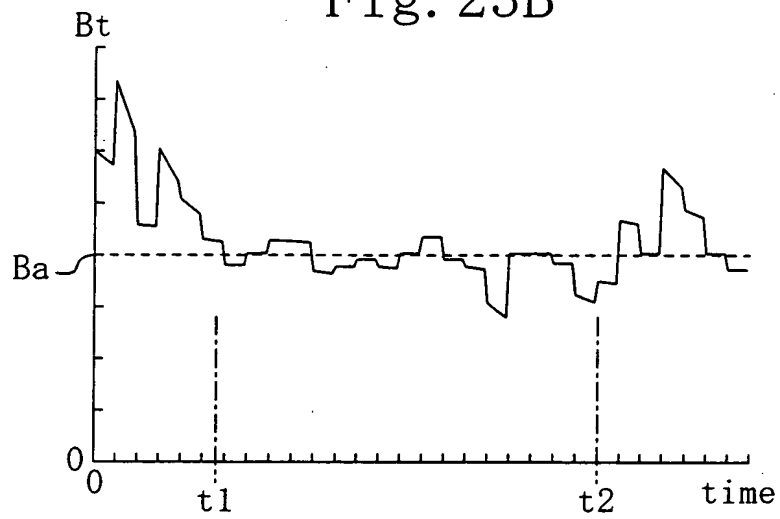


Fig. 23C

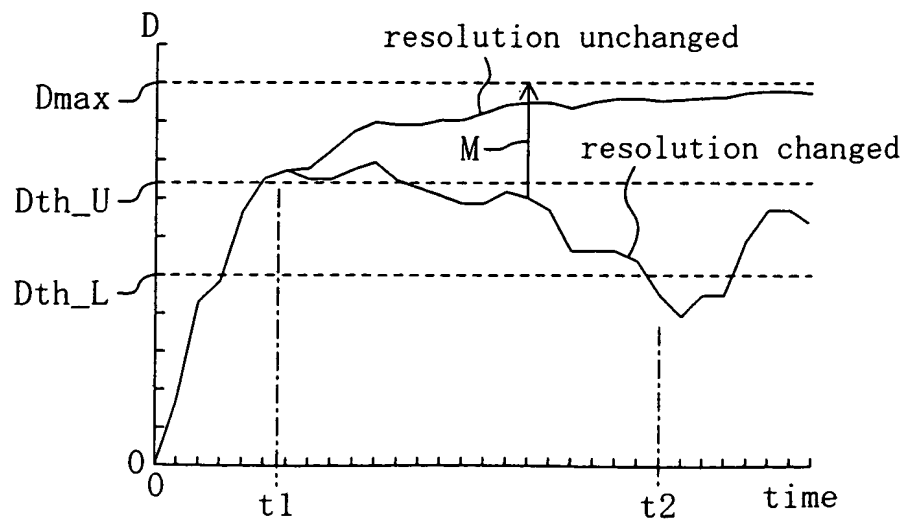


Fig. 24A

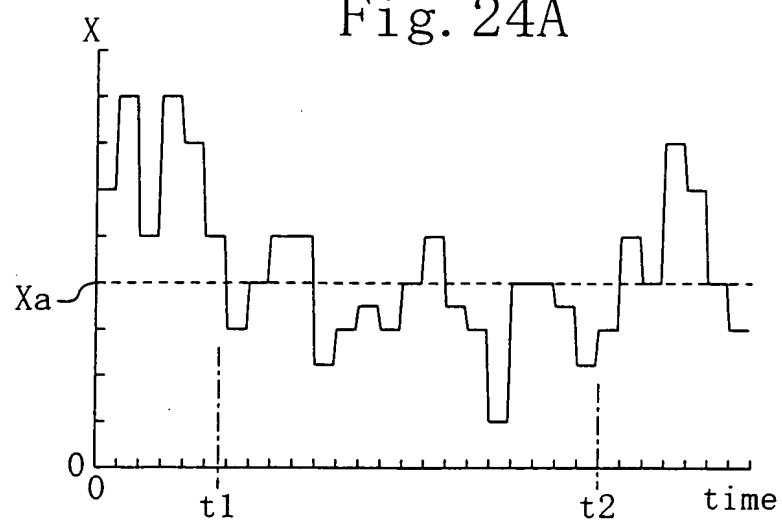


Fig. 24B

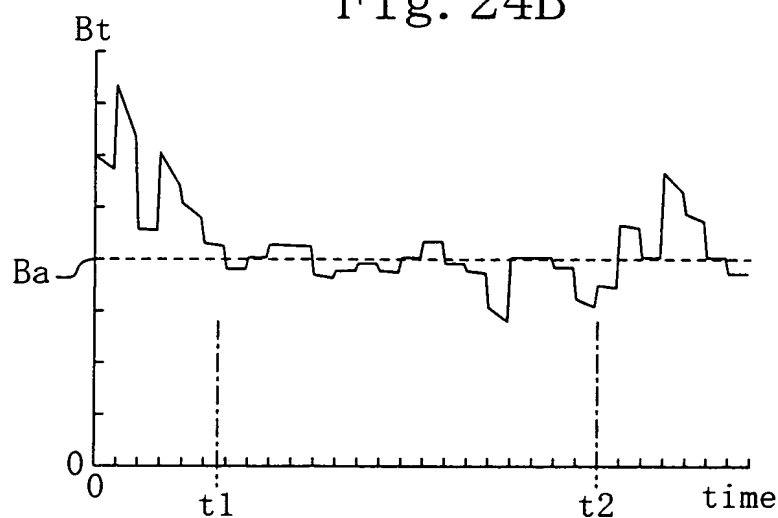


Fig. 24C

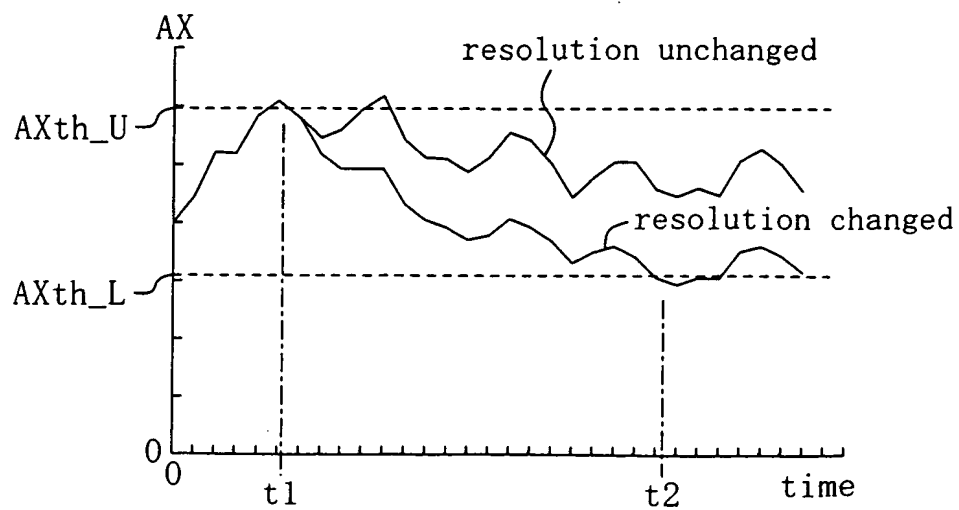


Fig. 25

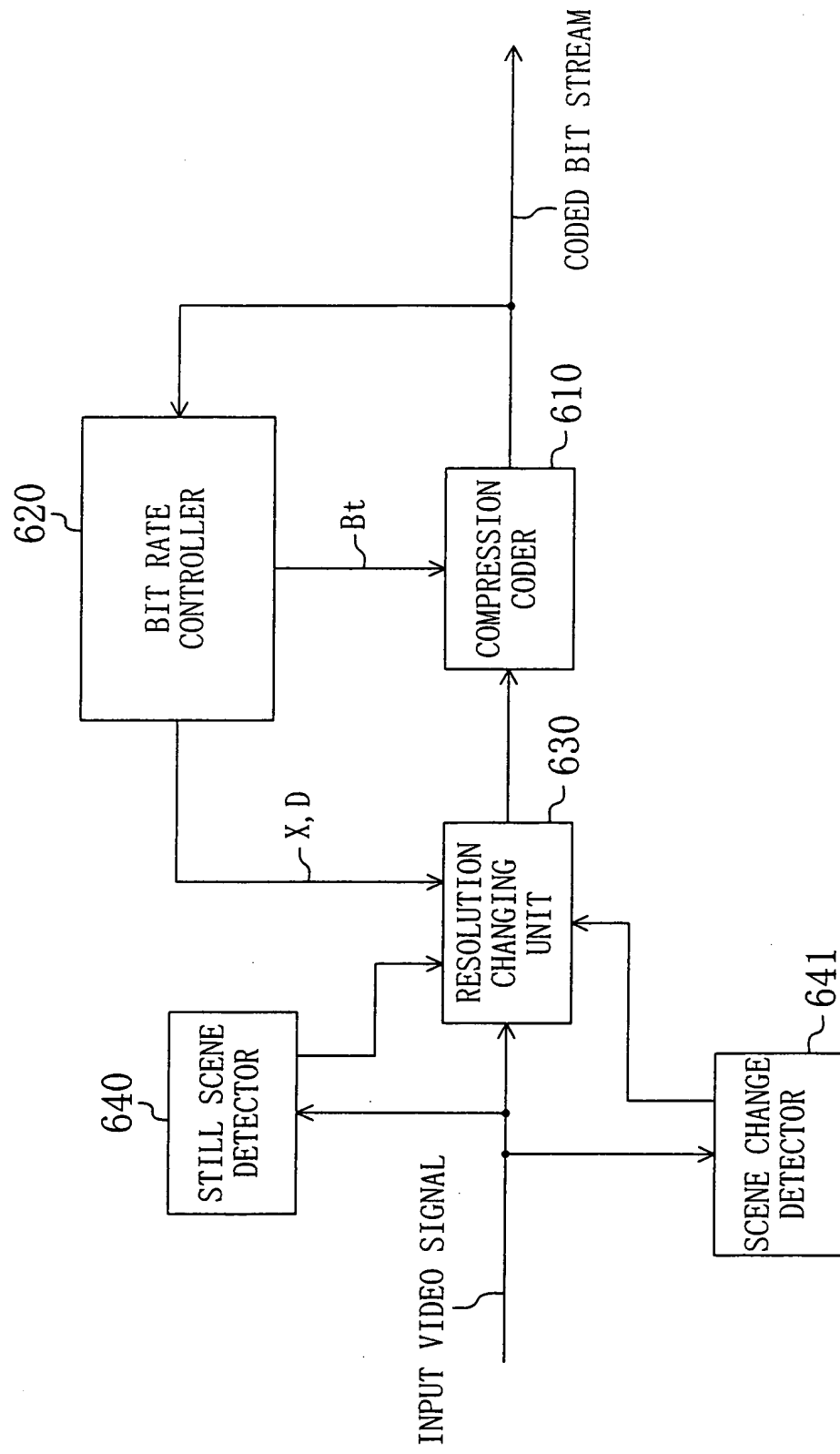
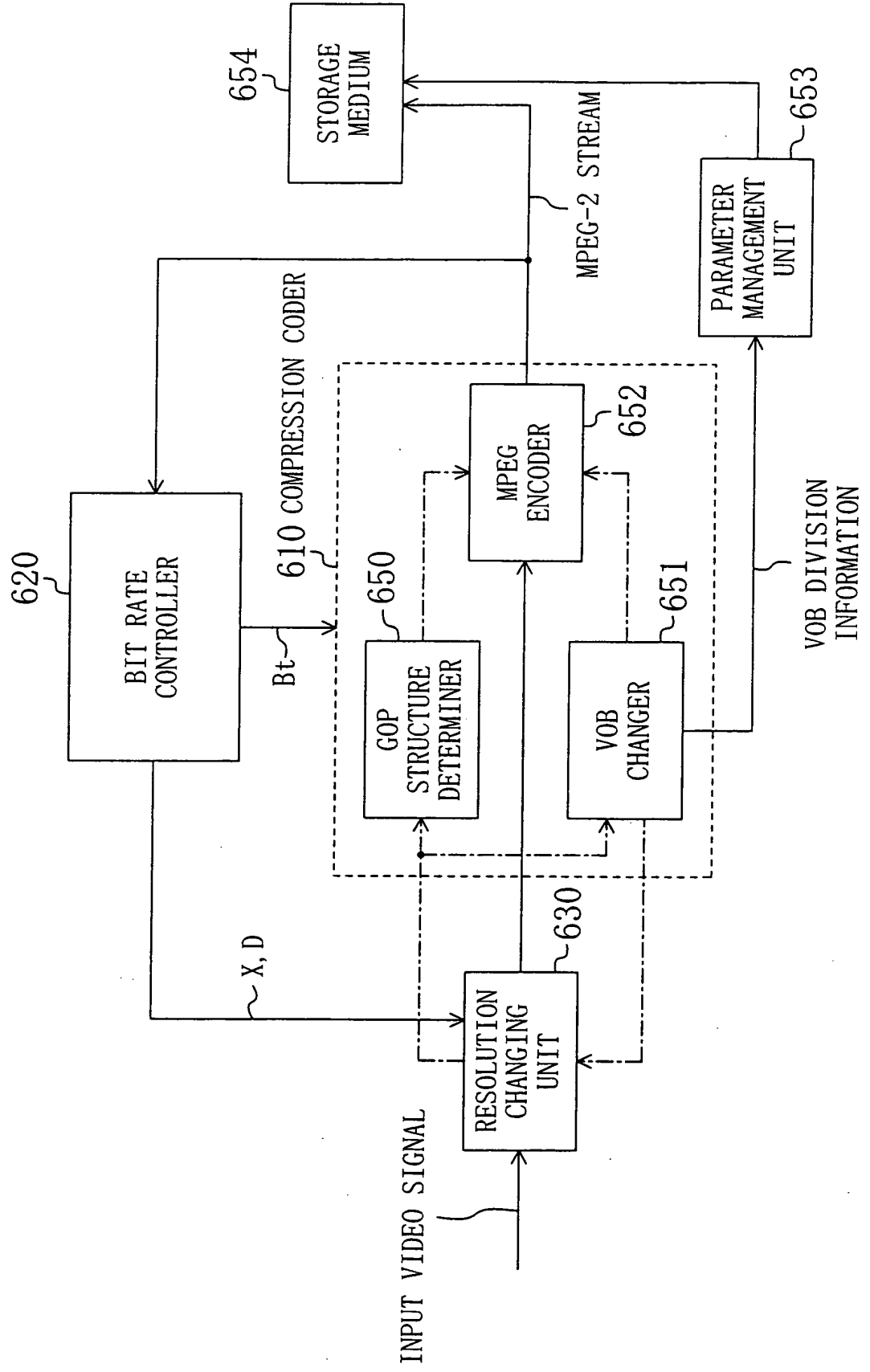


Fig. 26



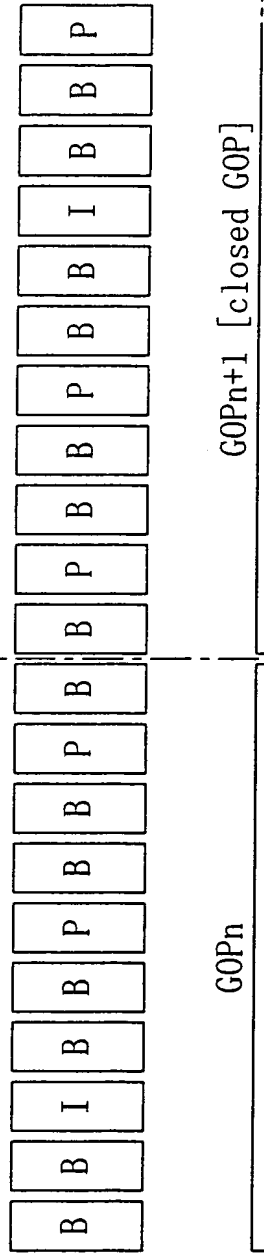
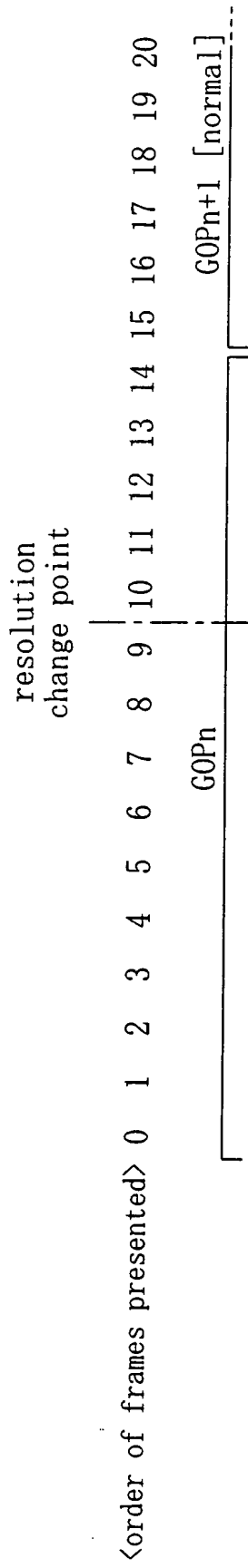


Fig. 27A

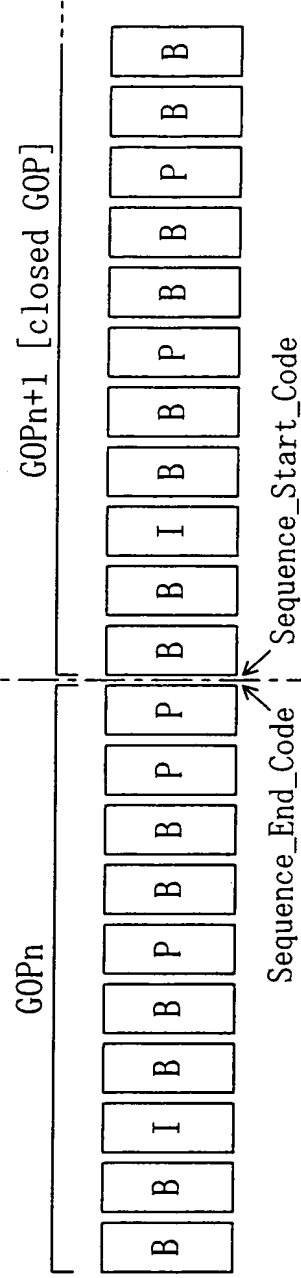


Fig. 27B

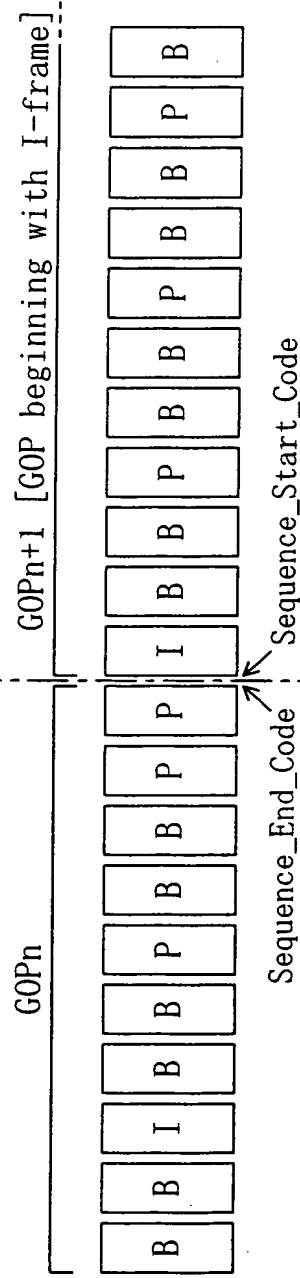


Fig. 27C

Fig. 28

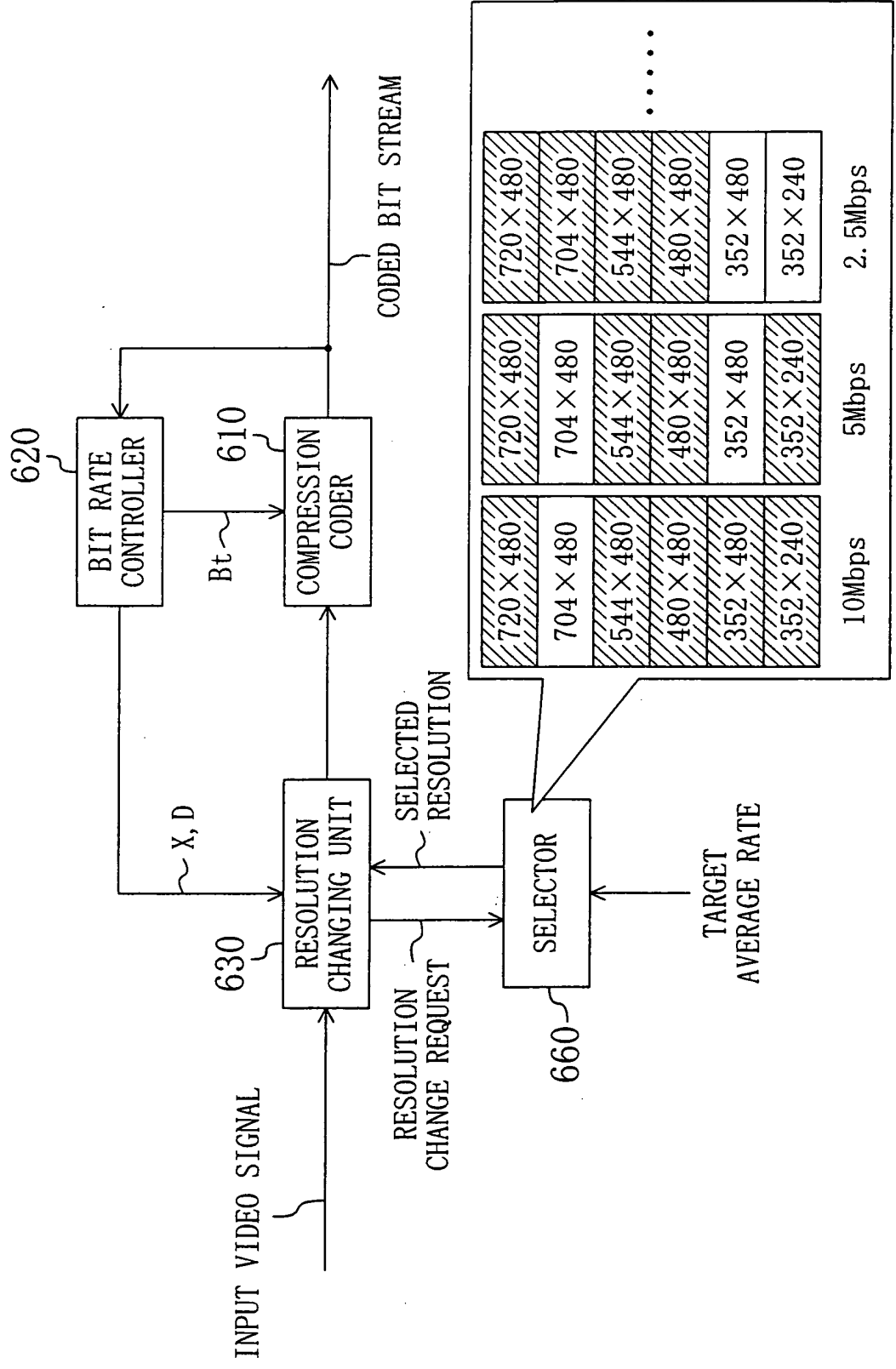


Fig. 29

